

GP65R12T4

N-channel 650V 120A GaN Power HEMT in TO247-4 Package

Preliminary Datasheet version: 1.0

Features

V_{DSS}	R_{dson}	I_{ds}	V_{GS}	$V_{GS(TH)}$
650V	12mΩ	120A	-3V to 15V	3.7V

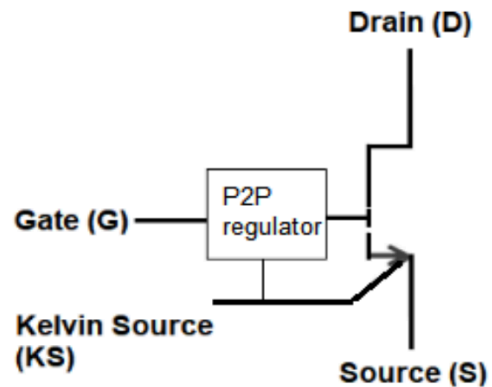
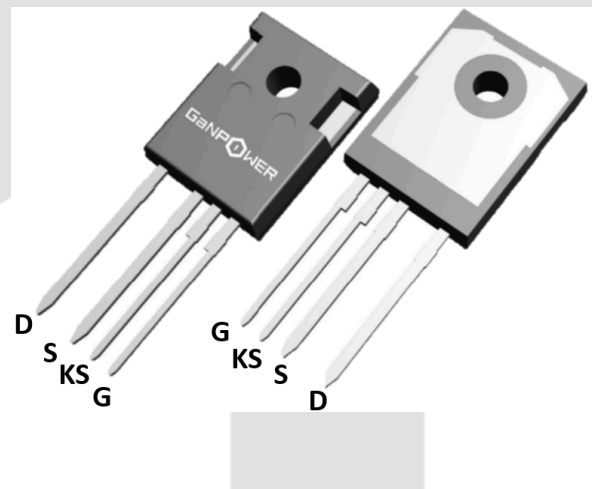
- Ultra-low R_{DSON}
- High dv/dt capability
- Extremely low input capacitance
- Zero Qrr
- Outstanding switching performance
- Low Profile
- **Upgraded P2P GaN with input regulator IC to match input lead and voltage of existing SiC MOSFET**

Applications

- Switching Power Applications
- Server and Telecom Power Application
- EV OBC and DC-DC Converters UPS, Inverters, PV

Description

These devices are N-channel 650V Power GaN HEMTs based on proprietary E-mode GaN on silicon technology, integrating an input regulator circuit to match input lead and voltage of existing SiC MOSFET in a pin-to-pin (P2P) fashion. The resulting product has extremely low on-state resistance, very low input capacitance and zero reverse recovery charge, making it especially suitable for applications which require superior power density, ultra-high switching frequency and outstanding efficiency.





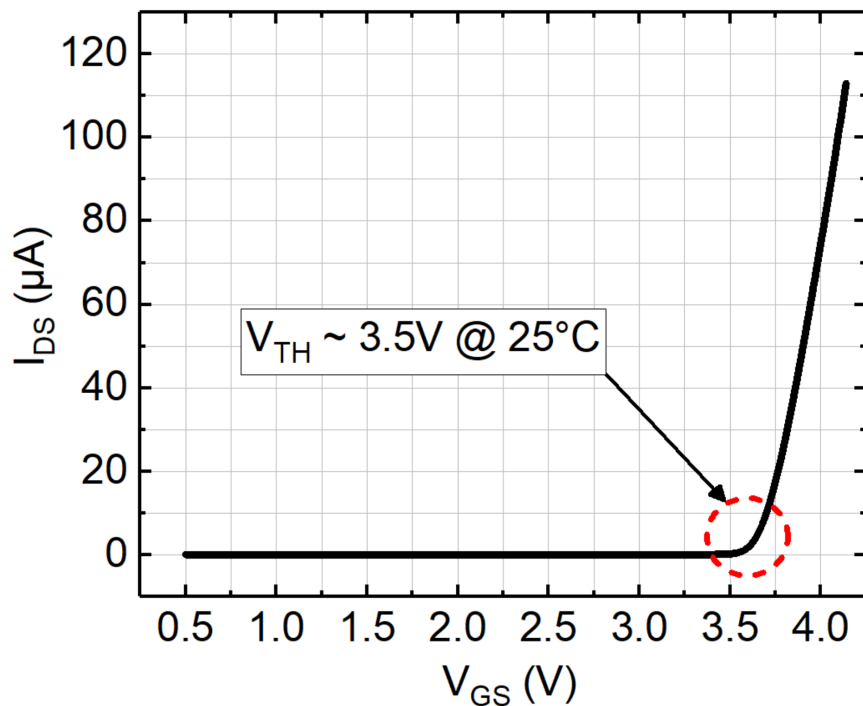
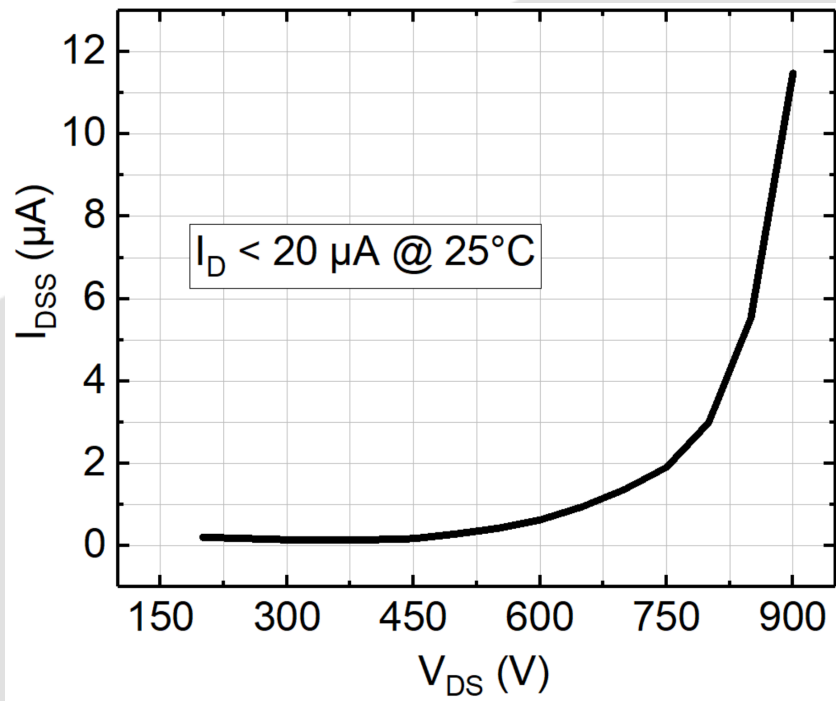
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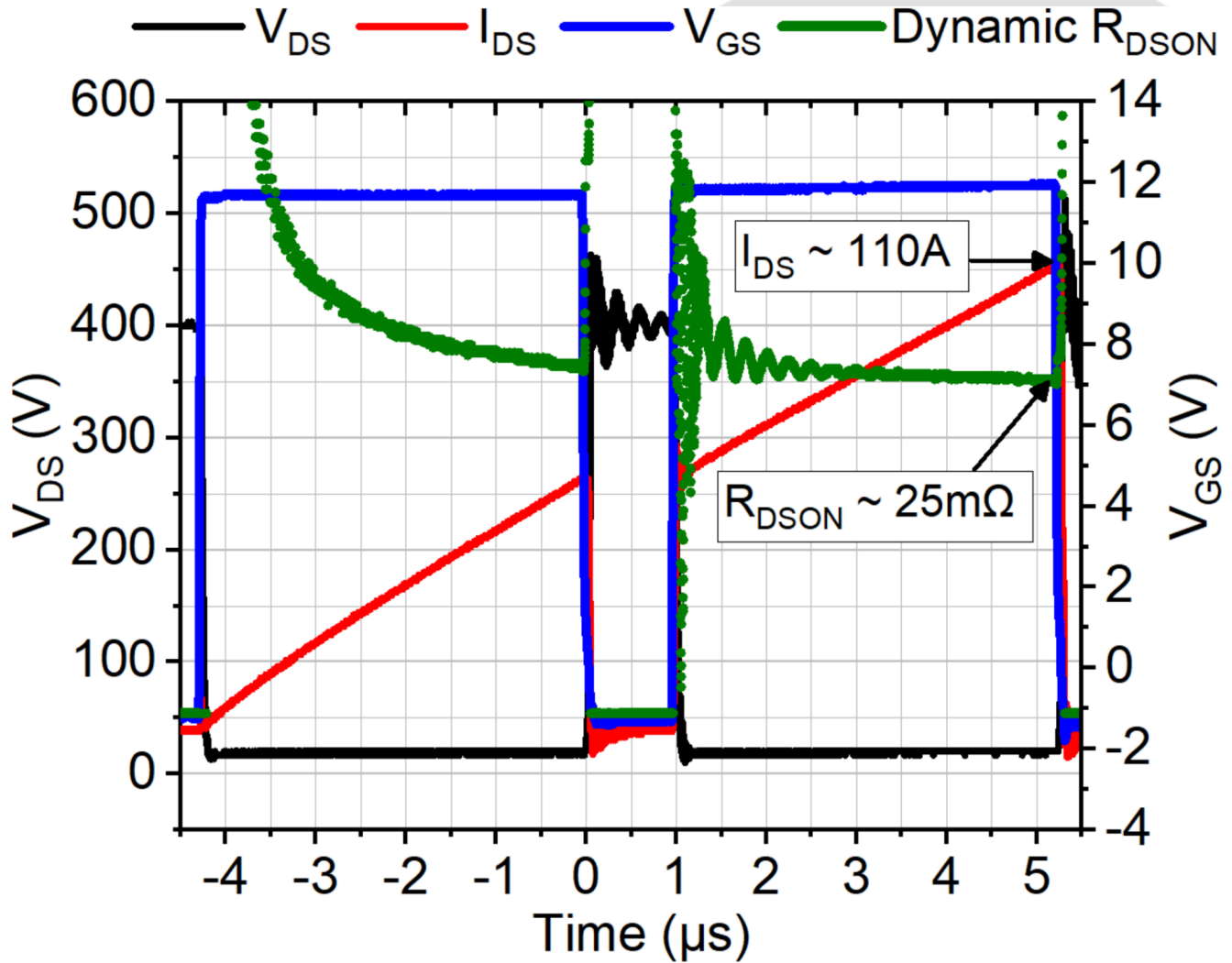
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Device Characteristics

Basic Parameters				Test data				
	Parameters		Conditions	Min	Typical	Max	Unit	
1	$V_{GS(TH)}$	Zero gate voltage drain current, $T_c = 25^\circ\text{C}$	$V_{DS} = V_{GS}$ $I_D = 3\text{mA}$	3.5	3.7		V	
2	BV_{DSS}	Drain-Source breakdown voltage	$V_{GS} = 0\text{V}$ $I_D < 20\mu\text{A}$		650	900	V	
3	I_{DSS}	Zero gate voltage drain current, $T_c = 25^\circ\text{C}$	$V_{GS} = 0\text{V}$ $V_{DS} = 650\text{V}$		0.8	3.0	μA	
4	I_{GSS}	Gate-Source Leakage @ 25°C	$V_{GS} = 12\text{V}$ $V_{DS} = 0\text{V}$		45	70	μA	
5	$R_{DS(on)}$	Static drain-source on resistance, $T_c = 25^\circ\text{C}$	$V_{GS} = 12\text{V}$	12	15	20	$\text{m}\Omega$	
Switching Performance				Test data				
	Parameters		Conditions	Min	Typical	Max	Unit	
1	$t_{D(ON)}$	Turn-on delay time	$V_{DS} = 400\text{V}$ $I_D = 60\text{A}$ $V_{GS} = +12\text{V}/-1\text{V}$ $R_{GON} = 2\Omega$ $R_{GOFF} = 5\Omega$		21		ns	
2	t_R	Rise time				51		ns
3	$t_{D(OFF)}$	Turn-off delay time				62		ns
4	t_F	Fall time				24		ns
5	E_{ON}	Switching energy during turn-on				600		μJ
6	E_{OFF}	Switching energy during turn-off				229		μJ

Electrical Performance





DPT at 25°C: $V_{BUS} = 400V$, L-load = 20 μH



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Further Information

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Data Source– Data here are based on recent tests but all parameters may not be up to date. Actual final test data from packaging production are available for selected customers upon request.