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GPI65015TO

N-channel 650V 15A GaN Power HEMT in TO220 Package

Datasheet version: 2.2

Features

BV _{dss}	R _{dson}	l _{ds}	Qg
650 V	92 mΩ	15 A	3.3 nC

- Ultra-low RDS(on)
- High dv/dt capability
- Extremely low input capacitance
- Zero Qrr
- Outstanding switching performance
- Low Profile

Applications

- Switching Power Applications
- Adapters, Quick Chargers

Gate (G) Source (S)

Description

These devices are N-channel 650 V Power GaN HEMTs based on proprietary E-mode GaN on silicon technology. The resulting product has extremely low on state resistance, very low input capacitance and zero reverse recovery charge making it especially suitable for applications which require superior power density, ultra-high switching frequency and outstanding efficiency.



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Device Characteristics

Static Parameters				Test data			
	Parameters		Conditions	Min	Typical	Max	Unit
1	$V_{gs(TH)}$	Gate threshold voltage	V _{ds} =V _{gs} Id= 10 mA	0.9	1.2	2.9	V
2	BV _{dss}	Drain-Source breakdown voltage	V _{gs} =0V I _d <25uA		650		V
3	I _{dss}	Zero gate voltage drain current, T_C = 25 C°	V _{gs} =0V V _{ds} =650V		0.1	23	uA
4	I _{gss}	Gate-Source Leakage @ 25°C	$V_{gs} = 6V$ $V_{ds} = 0V$		40	1500	uA
5	R _{dson}	Static drain-source on resistance, $T_C = 25C^{\circ}$	V _{gs} =6V I _d = 3 A		92	110	mΩ
6	V_{sd}	Reverse conduction voltage	I _{sd} = 0.4A V _{gs} =0V	1.2	1.8	3.0	V
7	R _g	Gate resistance	f=25MHz, Open drain		1.65		Ω
Dyr	namic Paramet	ers		Test data			
	Parameters		Conditions	Min	Typical	Max	Unit
	C _{iss}	Input capacitance	V _{gs} =0V		123		pf
1	C _{oss}	Output capacitance	V _{ds} =400V		29		pf
	C _{rss}	Reverse transfer capacitance	f=1MHz		4		pf
	Qg	Gate charge	V _{ds} =400V		3.3		nC
3	Q _{gs}	Gate to source charge	I _d =7.5A		0.5	7	nC
	Q_{gd}	Gate to drain charge	V _{gs} =6V		0.8		nC
2	Q _{rr}	Reverse recovery charge			0		nC
Switching Performance			Test data				
	Parameters		Conditions	Min	Typical	Max	Unit
1	t _{d(on)}	Turn-on delay time	V _{ds} =400V		5.3		ns
2	t _r	Rise time	I _d =2.5A		12		ns
3	t _{d(off)}	Turn-off delay time	R _g =10Ω		18		ns
4			V _{gs} =6V		13		ns



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Absolute Max. Ratings

	Symbols	Parameters	Value	Unit
1	V _{DS-max}	Breakdown voltage transient @ T _{case} =25°C	800	V
2	V_{GS-max}	Gate to source max. transient voltage @ T _{case} =25°C	-12 to +7.5	V
3	I _{ds-max}	Drain to source DC current @ T _{case} =25°C	15	А
4	l _{ds-max}	Drain to source DC current @ T _{case} =100°C	12	А
5	dv/dt _{-max}	Drain to source voltage slew rate	200	V/nS
6	T _{J-max} Max junction temperature		150	°C
7	$T_{S-storage}$	Storage temperature	-55 to 150	°C

Thermal and Soldering Characteristics (Typical)

	Symbols	Parameters	Value	Unit
1	R_{thJC}	Thermal resistance (junction to case)	1.2	°C /W
2	R_{thJA}	Thermal resistance (junction to ambient)	60	°C /W
2	T_{solder}	Reflow soldering temperature	260	°C

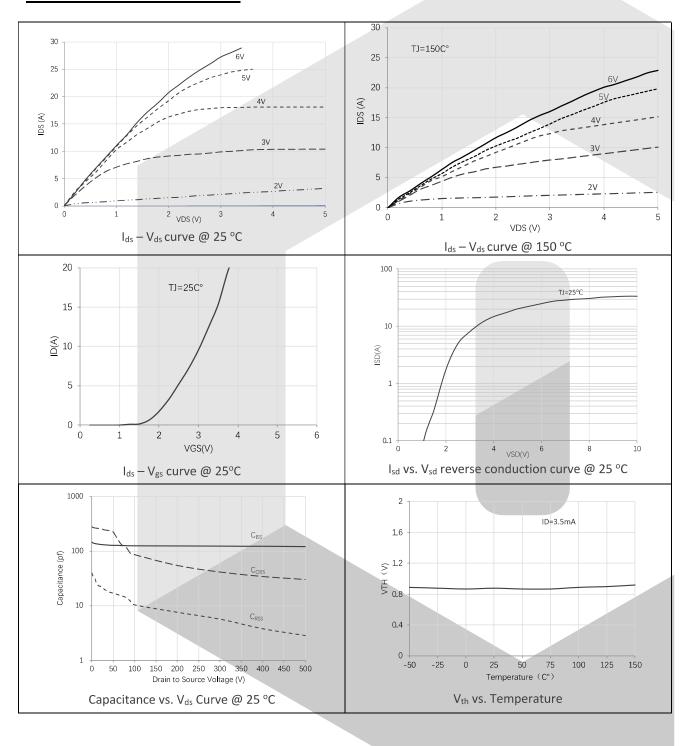
Ordering

Order Code	Package Type	Packaging Method	Qty
GPI65015TO	TO-220-3		



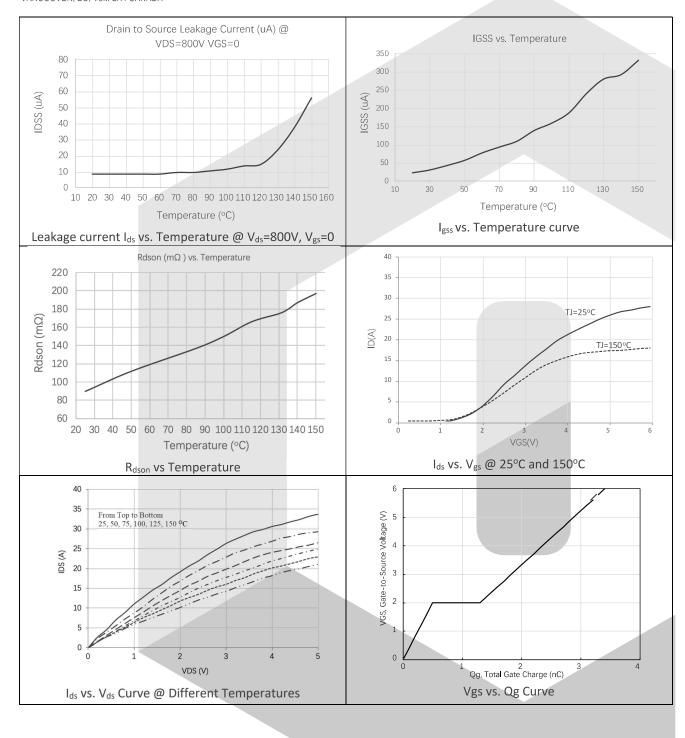
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Electrical Performance





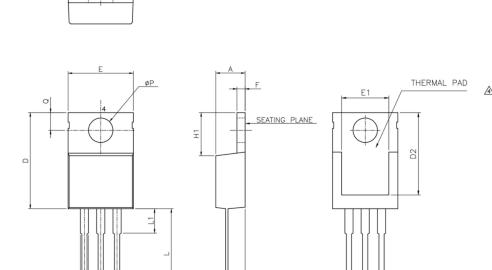
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Package Information



VARIATIONS (ALL DIMENSIONS SHOWN IN MM)

SYMBOLS	MIN.	MAX.
Α	3.56	4.82
b1	1.15	1.77
b	0.38	1.01
С	0.35	0.61
D	14.23	16.51
E	9.66	10.66
D2	11.75	11.90
E1	6.86	8.90
е	2.29	2.79
e1	4.83	5.33
F	0.51	1.39
H1	5.85	6.85
J1	2.04	2.92
L	12.70	14.73
øΡ	3.54	4.08
Q	2.54	3.42
L1	3.65	6.35
	A b1 b c c D E D2 E1 e e e1 F H1 J1 L ØP Q	A 3.56 b1 1.15 b 0.38 c 0.35 D 14.23 E 9.66 D2 11.75 E1 6.86 e 2.29 e1 4.83 F 0.51 H1 5.85 J1 2.04 L 12.70 ØP 3.54 Q 2.54

NOTES:

1. JEDEC OUTLINE : N/A.



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Further Information

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Data Source— Data here are based on recent tests but all parameters may not be up to date. Actual final test data from packaging production are available for selected customers upon request.

