

## GPI65008IC GaN Power IC in SOP8 Package

Preliminary Datasheet version: 1.0

### Features

$BV_{dss}$	$R_{dson}$	$I_{ds}$
650V	170 m $\Omega$	7.5 A

- Ultra-low  $R_{DS(on)}$
- High  $dv/dt$  capability
- Extremely low input capacitance
- Fast switching
- Low Profile

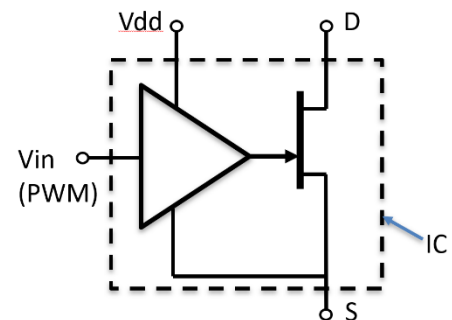
### Applications

- Switching Power Applications
- Power adapters and power delivery chargers

### Description

These devices are power IC based on 650 V Power GaN HEMTs using proprietary E-mode GaN on silicon technology. The gate driver is integrated with the main power transistor resulting in fast switching, high system power density and low cost.

### Device Characteristics





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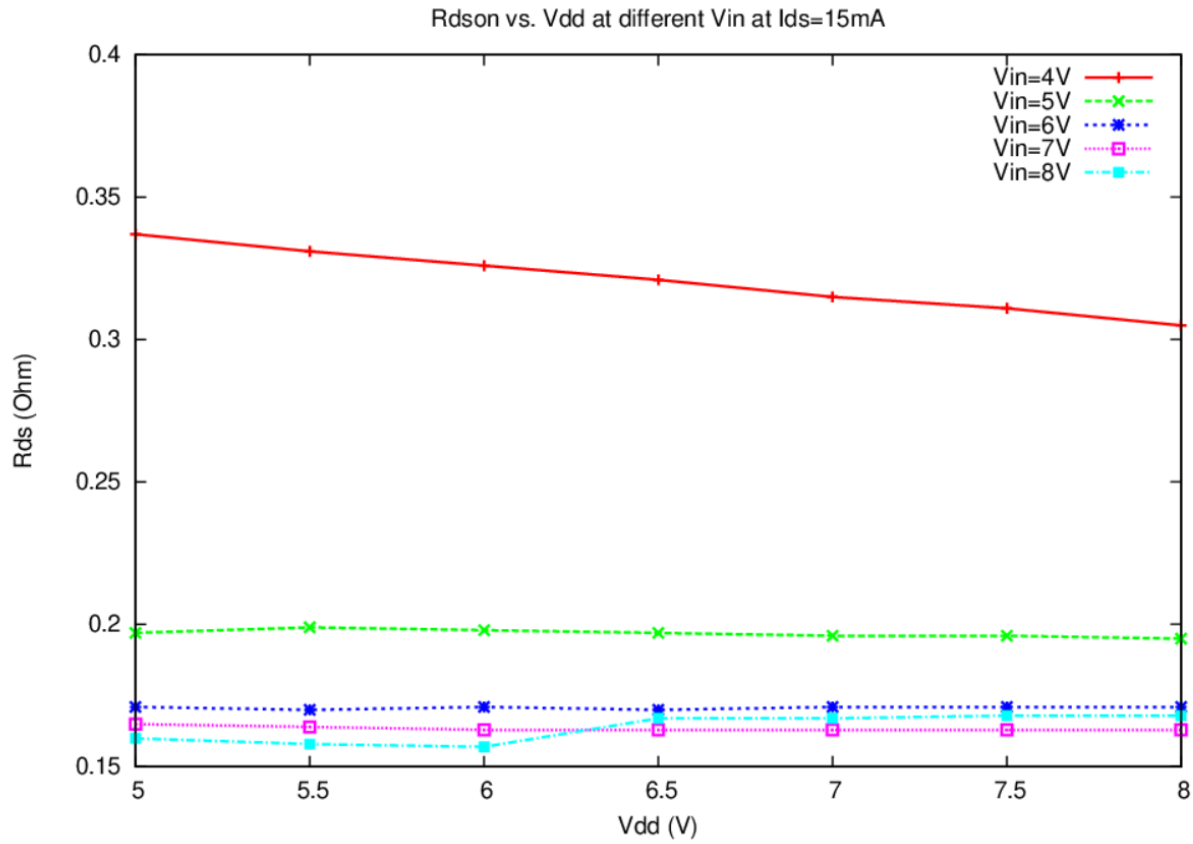
Basic Parameters				Test data			
	Parameters		Conditions	Min	Typical	Max	Unit
1	$BV_{dss}$	Drain-Source breakdown voltage	$V_{gs}=0V$ $I_d=10\mu A$		650		V
2	$R_{dson}$	Static drain-source on resistance, $T_C=25^\circ C$	$V_{gs}=6V$ $I_d=1.8A$	165	180	190	m $\Omega$
3	Vdd	Drive supply voltage		5	6.5	8	
4	Vin	PWM input pin voltage		4	6.5	8	
Switching Performance				Test data			
	Parameters		Conditions	Min	Typical	Max	Unit
1	$t_{d(on)}$	Turn-on delay time	$V_{ds}=350V$ $I_d=2.5A$ $V_{in}=5V$ $V_{dd}=6.5V$		15		ns
2	$t_r$	Rise time			10		ns
3	$t_{d(off)}$	Turn-off delay time			12		ns
4	$t_f$	Fall time			10		ns



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## Electrical Performance

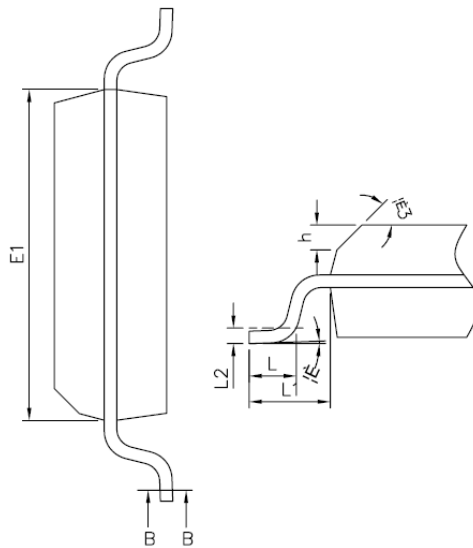
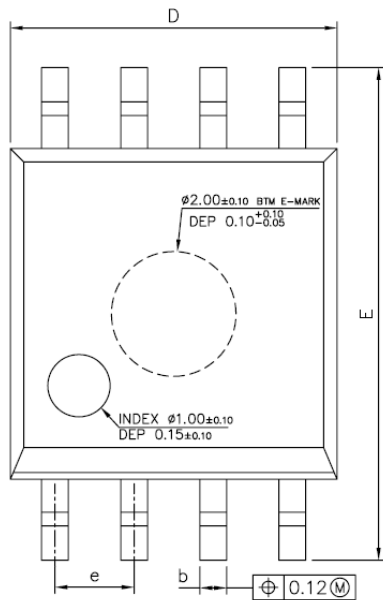




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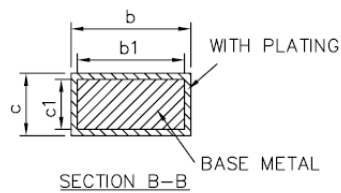
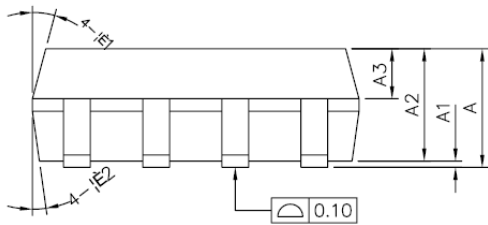
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## Package Information



COMMON DIMENSIONS  
 (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	-	-	2.10
A1	0.05	0.10	0.25
A2	1.70	1.80	1.90
A3	0.70	0.80	0.90
b	0.39	-	0.52
b1	0.38	0.43	0.48
c	0.20	-	0.25
c1	0.19	0.20	0.21
D	5.14	5.24	5.34
E	7.70	-	8.10
E1	5.20	5.30	5.40
e	1.17	1.27	1.37
h	0.30	0.40	0.50
L	0.55	0.75	0.85
L1	1.30REF		
L2	0.25BSC		
$\theta$	0°	-	8°
$\theta_1$	13°	15°	17°
$\theta_2$	6°	8°	10°
$\theta_3$	43°	45°	47°





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## GaN HEMT Frequently Asked Questions

1	<p><b>Q: Can we do pin to pin switch for silicon MOSFET or IGBT?</b></p> <p>A: The short answer is no. GaN HEMT power devices are far superior than the best silicon devices such as super junction MOSFETs. However, due to different requirements of gate driving voltage and extremely high dv/dt slew rate, special drivers and optimized PCB layouts are recommended to minimize the impact from circuit parasitics. Some packaging forms such as GaNPower's DFN packaged devices offer both sense and force for the source terminal. Also, for traditional TO220 packages, please be advised that the pins are arranged as Gate – Source -Drain, and the thermal pad is connected to the source instead of drain.</p>
2	<p><b>Q: Are GaN power devices reliable?</b></p> <p>A: GaN power HEMTs have been tested by GaNPower and many other vendors, users and testing facilities to be as reliable (if not better than) silicon counterparts.</p>
3	<p><b>Q: How do GaN power devices compare with SiC?</b></p> <p>A: Currently GaN power HEMT devices are most suitable for low to medium voltage (<math>\leq 1200V</math>) and power (&lt;20KW) applications. GaN is the ideal choice for high frequency applications. SiC devices are better choice for high voltage and high-power applications (&gt;20KW).</p>
4	<p><b>Q: Do we need to parallel an FRD for applications such as inverters?</b></p> <p>A: GaN devices are different from silicon MOSFET or IGBT in that they have no inherent PN junction diodes that cause reverse recovery issue. User do not need to parallel an FRD for the purpose of suppressing the body diode reverse recovery effect, since GaN HEMT can operate in both first and third quadrants. However, care should be taken for the dead time power loss since the Vsd voltage of GaN HEMT is usually close to 2V. This is especially true when a negative gate voltage is applied.</p>
6	<p><b>Q: Can we parallel GaN HEMT devices?</b></p> <p>A: Yes, GaN HEMT is ideal for paralleling, due to positive temperature coefficient of Rds(on) and slightly positive temperature coefficient of threshold voltage.</p>
5	<p><b>Q: Where can we find drivers for GaNPower HEMT devices?</b></p> <p>A: While some of the GaNPower's HEMTs are either monolithically integrated with gate driver or co-packaged with a silicon driver, drivers can be easily found from vendors such as TI and Silicon Lab for either single sided or half-bridge configurations:</p> <ul style="list-style-type: none"><li>✓ <a href="#">TI: LM5114</a>: Single 7.6A Peak Current Low-Side Gate Driver</li><li>✓ <a href="#">TI: UCC27611</a>: 5V, 4A/6A Low Side GaN Driver</li><li>✓ <a href="#">Maxim: MAX5048C</a>: 7A Sink/3A Source Current, 8ns, SOT23, MOSFET Drive</li><li>✓ <a href="#">Fairchild: FAN3122</a>: Single 9-A High-Speed, Low-Side Gate Driver</li><li>✓ <a href="#">Silicon Lab: Si827X</a>: 4 Amp ISO driver with High Transient (dv/dt) Immunity</li></ul>